

MEMORY MODULE SELF IDENTIFICATION

Abstract

A memory module is described having a memory array storing data, an ID information output circuit for outputting ID information for identifying memory modules, and output switching means for selectively switching between output from the memory array and output from the ID information output circuit, and output from the ID information output circuit will be selected instead of output from the memory array until the memory module is initially written after the power supply to the memory module has been started. This enables a memory module to be identified without having to add parts to a computer system unit or providing a ROM storing a specification or the like to the memory module.